Debdoot Sheet

B Tech

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Date of Birth: January 25, 1986

Sex: MALE

Citizenship: Indian

Marital Status: Single

PROFILE

Debdoot Sheet is an Engineer. He was born in 1986 at Kharagpur, West Bengal, INDIA. He has Graduated with Electronics & Communication Engineering from Institute of Engineering & Management under The West Bengal University of Technology leading to Bachelor of Technology. He is credited to having achieved a WBJEE rank of 832 while joining the institute for his under-graduate studies. Academic achievements include a steady academic record of high grades through Basic & Professional courses. He is also credited to having a wide scale achievement base in co-curricular & extra-curricular fields. With keen interest in Research activities he has been working in the fields of Bio-Medical Embedded Systems, Signal Processing, Renewable Energy Resources and RFID and has also published his findings as technical articles at leading National and International Conferences and Journals.

EDUCATION

Professional Education

Degree: Bachelor of Technology
Discipline: Electronics & Communication Engineering
College: Institute of Engineering & Management, Salt Lake, Kolkata. INDIA. 700 091.
University: West Bengal University of Technology, Salt Lake, Kolkata. INDIA. 700 064.
DGPA: 8.78/10
Year of passing: 2008

Semester	I	II	III	IV	V	VI	VII	VIII
SGPA	8.67	8.60	8.67	8.54	8.50	9.24	9.08	8.72

Basic Education

Council: Council for the Indian School Certificate Examinations, New Delhi. (CISCE)
Certificate: Indian School Certificate (Year – 12)
School: S E Rly. Mixed Hr. Sec. School (E M), Kharagpur. INDIA. 721 301.
Percentage: 80.83 %
Year of passing: 2004

Council: Council for the Indian School Certificate Examinations, New Delhi. (CISCE)
Certificate: Indian Certificate of Secondary Education (Year – 10)
School: Sacred Heart High School, Kharagpur. INDIA. 721 301.
Percentage: 86.16 %
Year of passing: 2002

Passport No: G6553253

Voter's Id: HSL2942613

PAN Card no: BARPS3112D

PROFICIENCIES

Programming languages

Visual Basic, C, Assembly Language

Simulation Software

Matlab & Simulink, Proteus, NI Electronics Work Bench, NI LabVIEW, Circuit Maker, VHDL, EAGLE, pSpice, AVR Studio, MPLAB, TI Code Composer Studio.

Hardware Platforms

Intel (8085, x86, MCS-51, MCS-96), AVR, PIC, Parallax (Basic Stamp Modules), Texas Instruments (TMS320C5416, TMS320C6713, TMX320C6455)

*Recipient of the **Teaching Assistanceship (TA)** Program (2008) while in final year of Under-Graduate studies at Institute of Engineering & Management, Kolkata, India.*

ACHIEVEMENTS

- ELESYS – IEM Technical Festival – 2007.
- SERMSTS Mathematics Olympiad – 2003.
- SERMSTS Science Model Exhibition – 2003.
- TERI Green Olympiad – 2001 & 2000.
- National Science Olympiad – 2001.
- Science Talent Search Examination – 2002.
- Talent Search Institute – Chemistry, Mathematics – 2001 & 2000.

PROJECTS

RFID System Design & Application
Guides: Tirthankar Datta (Asst. Prof. ECE, IEM) & Dr. Subir Kr Sarkar (Prof. ETC, JU)
Members: **Debdoot Sheet**, Debanjan Das, Chandra Vikas, Budhaditya Saha (IEM)
Duration: Aug 2007 – May 2008.

Adaptive Delta Modulation for Transmission of ECG Signals
Guide: Dr. Ajay Kr Ray (Prof. E&ECE, Head-SMST, IIT Kgp)
Member: Debdoot Sheet (IEM)
Duration: July 2007 .

Synthetica
Guide: Dr. Manish Kr Mukherjee (Prof. ECE, Director-Academic, IEM)
Members: **Debdoot Sheet**, Indranil Das (IEM)
Duration: Feb – Mar 2007.

Peritoneal Dialyser Automation
Guide: Dr. Ajay Kr Ray (Prof. E&ECE, Head-SMST, IIT Kgp)
Member: Debdoot Sheet (IEM)
Duration: July 2006 .

Electronic Thermometer
Guides: Arindam Chakraborty (Lec. ECE, IEM) & Sanjay Chatterjee (Lec. ECE, IEM)
Member: Debdoot Sheet (IEM).
Duration: Feb – Apr 2006.

PUBLICATIONS

Journal

Realization and Simulation of the Hardware for RFID System and its Performance study

Authors: **Debdoot Sheet**, Atul Kumar, Agnibesh Dutta, Samrat Das Gupta, Tirthankar Datta, Subir Kr. Sarkar.
Publication: *IET-UK Technical Journal (IETECH).*
Status: Accepted

Conference

RFID based Airport Logistics Management

Authors: Tirthankar Datta, **Debdoot Sheet**, Avik Kr Si, Sayar Dutta Biswas, Debdeep Ghosh.
Venue: *3rd Innovative Conference on Embedded Systems, Mobile Communication & Computing (ICEMC2 2008).* Infosys, Mysore, Karnataka, India. August, 11-14, 2008.
Status: Accepted

Voice Filtering over a Wideband Stereophonic Audio Signal

Author: Debdoot Sheet.
Venue: *2nd National Conference on VLSI & Communication (NCV-Com'08).* SAINTGITS College of Engineering, Kottayam, Kerala, India. March, 14-15, 2008.
Status: Published. pp. 24-28.

Realization and Simulation of the Hardware for RFID System and its Performance study

Authors: **Debdoot Sheet**, Atul Kumar, Agnibesh Dutta, Samrat Das Gupta, Tirthankar Datta, Subir Kr. Sarkar.
Venue: *IET-UK International Conference on Information and Communication Technology in Electrical Sciences (ICTES 2007).*Dr. M.G.R. University, Chennai, Tamil Nadu, India. December, 20-22, 2007.
Status: Published. pp. 697-700

*Initiated the Organizing of National Seminar on **RFID-The Cutting Edge Technology** (Apr 26, 2008) at IEM on behalf of IEM-RFID Research Cell.*

ACTIVITIES

- Student In-Charge. IEM RFID Research Cell. July 2007 – June 2008.
- Principle Secretary for Science & Technology. IEM Students' Gymkhana. Jul '07 – Jun'08.
- Student Coordinator. IEM e-cell & NEN. July 2007 – June 2008.
- Organizing secretary – IEM-NEN e-week. Feb 2-8, 2008.

REFERENCES

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Prof. Dr. Pinaki Mukherjee
Head, Dept. of Electronics & Communication Engineering
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